



Display

Date: **January 26, 1998**

Subject: **CORRECTING SMEAR**

Model: **VPH-G70Q**

Serial No: **UP TO 2,000,580**

DESCRIPTION

A high voltage current may undulate and a raster may become uneven when feeding a black window in white background, causing a smear to occur on the picture as shown in Figure 1.

To correct this condition, perform the following modification procedure.

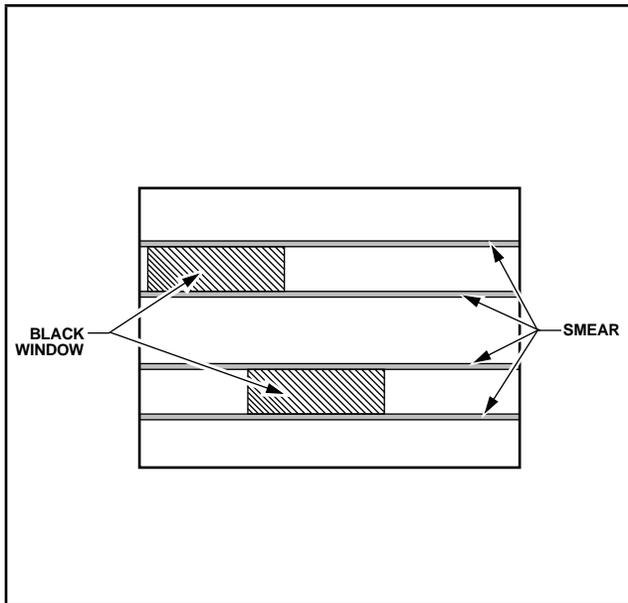


Figure 1

PARTS REQUIRED

Part No.	Description	Qty.
A-1343-405-A	DD Board, Suffix -12	1
1-900-233-29	IK Harness	1
1-900-233-49	6-Pin Connector Assy	1
1-249-441-11	Res, Carbon, 100kΩ, 1/2W	3

MODIFICATION PROCEDURE

1. Install new IK harness connecting MF board and DD board as follows and as shown in Figure 2:

- a. Solder lead ends of new IK harness to lands on MF board (component side) between CN220 and CN280, near screw hole as shown.
- b. Connect other end of IK harness to DD board-CN2.

NOTE: Do not remove connector assemblies between C boards and MF board, since connector assemblies are clamped to other connector assemblies.

2. Replace former suffix -11 DD board with new suffix -12 DD board.

NOTE: Apply RTV to CN1 on DD board, and CN361 on D board, connecting the two boards as shown in Figure 3.

D Board (Component Side, Zone A/B-18)
(See Figure 4.)

3. Connect 6-pin connector assembly as follows:
 - a. Connect one end of connector assembly to CN3 on DD board as shown.
 - b. Solder pin 3 (orange) lead to pin 10 of IC803.
 - c. Install three 100kΩ carbon resistors as follows:
 - Solder one lead of first resistor to thru-hole near pin 1 of IC807.
 - Solder one lead of second resistor to thru-hole near pin 6 of IC813.
 - Solder one lead of third resistor to thru-hole below R964.
 - d. Twist pin 1 (brown) lead of 6-pin connector assembly around unsoldered leads of carbon resistors, to bind leads together as shown in Figure 4, inset.

(Zone F-18)

- e. Solder pin 4 (yellow) lead to thru-hole of GND trace near TP1.



- f. Solder pin 6 (blue) lead to thru-hole of -12V bus near IC2.
- g. Solder pin 5 (green) lead to thru-hole of +12V bus near C2.

- c. Adjust RV2 so that level (a) becomes the same as level (b).
- d. Adjust RV3 so that level (c) becomes the same as level (b) and spike (d) is minimized as shown.
- e. Adjust RV1 so that level (e) becomes DC 0V±0.01V.
- f. Repeat sub-steps c through e to perform tracking.

CONFIRMATION

(See Figure 5.)

1. Adjust DD board suffix -12 as follows:
 - a. Set fH frequency to 64kHz, using internal oscillator or external signal.
 - b. Connect oscilloscope to TP1 and TP2 (GND).
2. Adjust registration for each channel, even if waveform adjustment has been performed, since waveform of DD board suffix -12 differs from that of suffix -11 board.

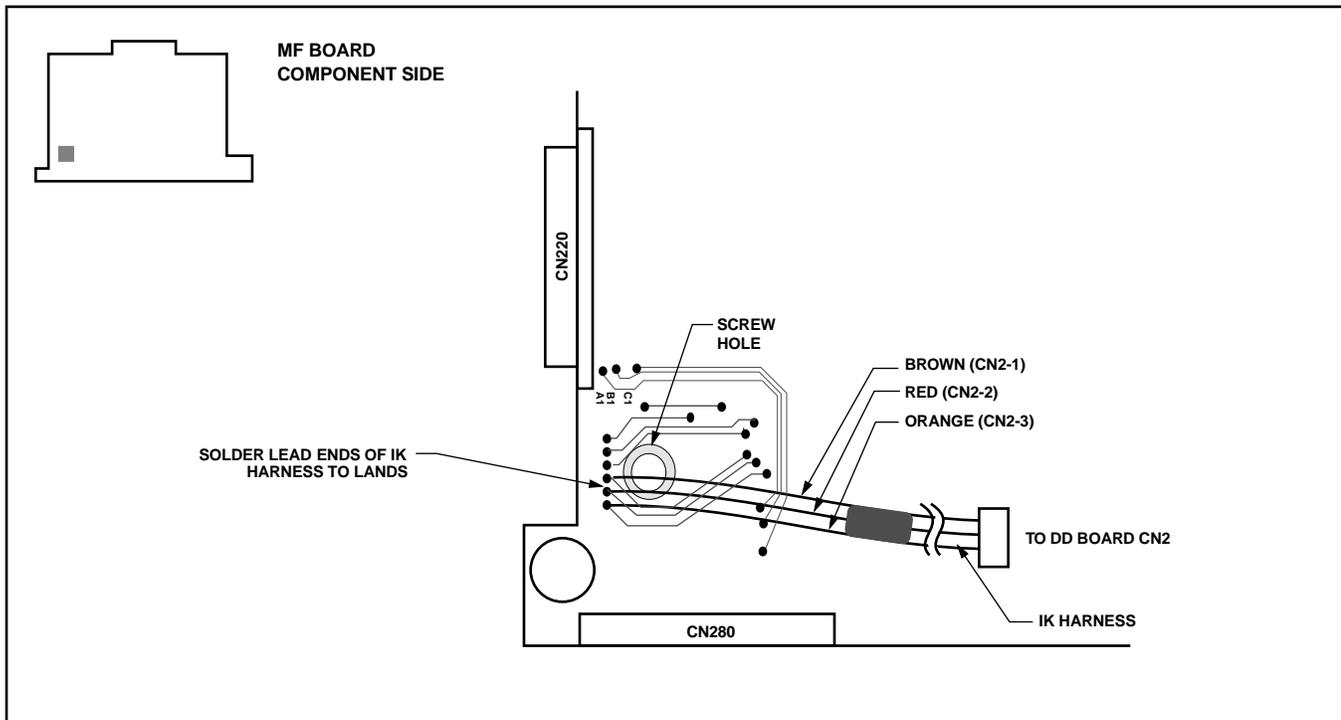


Figure 2

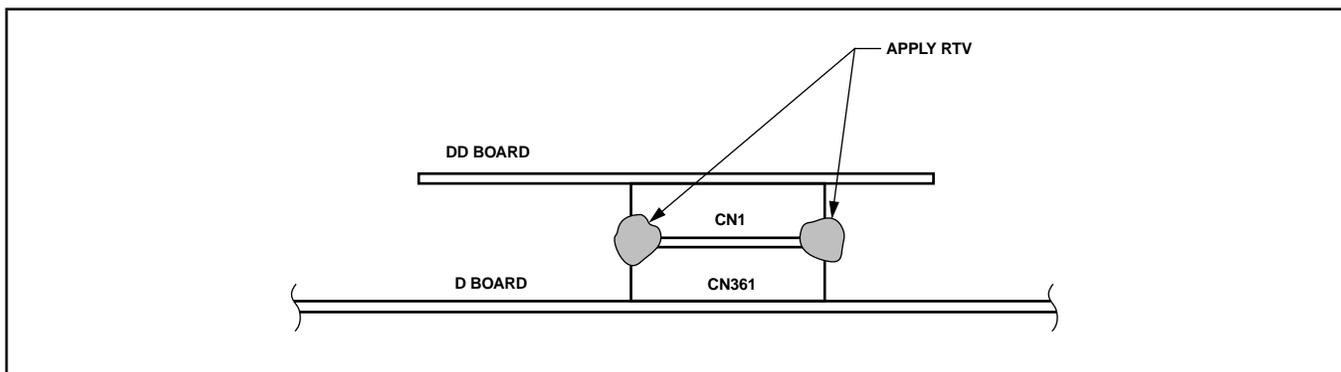


Figure 3

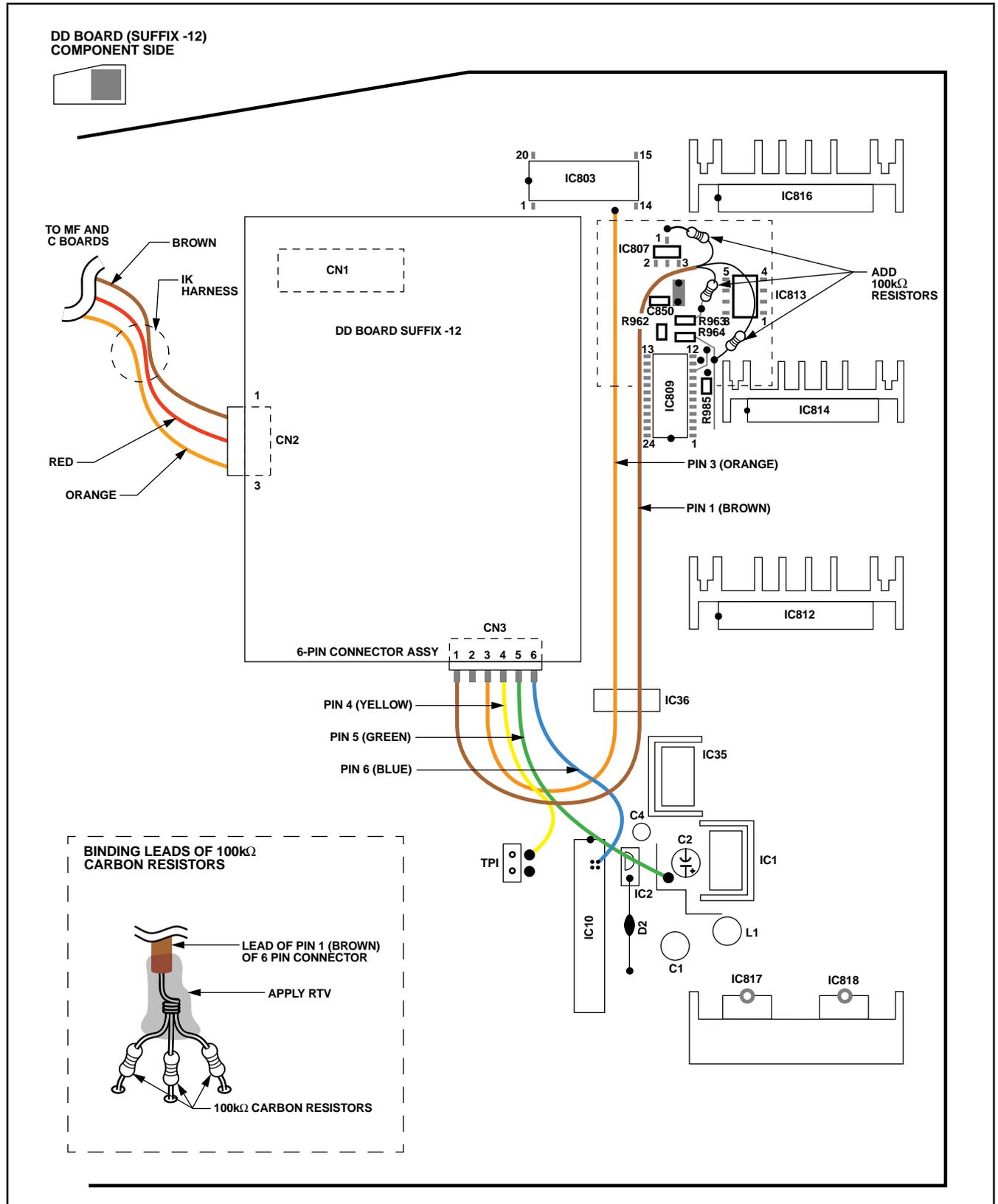


Figure 4

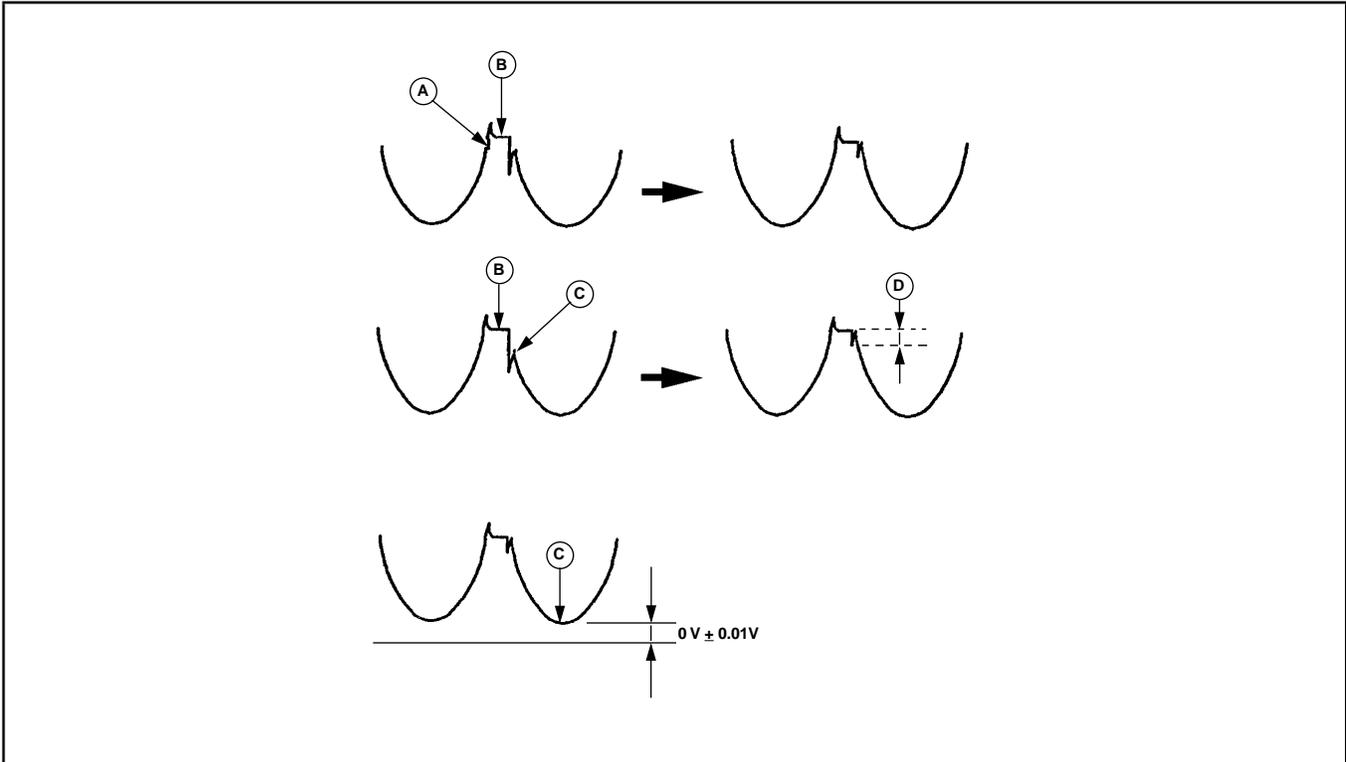


Figure 1